



TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
JP920030017

IN THE APPLICATION OF: Nakamura et al.

Application No. 10/709456	Filing Date 05/06/04	Examiner Unassigned	Customer No. 024241	Group Art Unit	Confirmation No.
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Title: DYNAMIC SEMICONDUCTOR MEMORY DEVICE AND BIT LINE PRECHARGE METHOD THEREFOR.

TO THE DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE:

Transmitted herewith is:

1. Certified copy of foreign patent application (Japanese Application No. JP2003-128367)
2. Return postcard
3. Information Disclosure Statement
4. PTO-1449

5. Cited Art
in the above identified application.

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